

Notice of Allowability	Application No.	Applicant(s)	
	09/590,172	BOHLIN, LARS	
	Examiner	Art Unit	
	Joseph D. Torres	2133	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to Request for Change of Address filed 21 January 2003.
2. ☒ The allowed claim(s) is/are 1-11.
3. ☒ The drawings filed on 09 June 2000 are accepted by the Examiner.
4. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
 6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|---|--|
| <ol style="list-style-type: none"> 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) 3. <input checked="" type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date <u>04/03/2001</u> 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | <ol style="list-style-type: none"> 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____ 7. <input type="checkbox"/> Examiner's Amendment/Comment 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance 9. <input type="checkbox"/> Other _____ |
|---|--|

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REASONS FOR ALLOWANCE

Claims 1-11 are allowed.

The following is an examiner's statement of reasons for allowance:

The present invention pertains to a method a method of supervising parallel processes in a data system that includes a first system CP-A and a second system CP-B, the method comprising steps for generating a first status word STWA in the first system CP-A and a parallel second status word STWB in the second system CP-B, generating in the first system CP-A a first check code CCA from the first status word STWA; and generating in the second system CP-B a second check code CCB from the second status word STWB, wherein said method is characterized by the further steps of transferring the first check code CCA from the first system CP-A to the second system CP-B.

Claim 1 recites various features:

"recreating the first data word STWA in the second system CP-B by evaluating the first check code CCA, the second check code CCB and the second data word STWB".

The Prior Art of record and, in particular, Dutton; Drew J. et al. (US 6357024 B1, hereafter referred to as Dutton), teach a method of supervising parallel processes in a data system that includes a first system CP-A and a second system CP-B (Figure 4 of Dutton teaches a first CPU CP-A 62a and a second CPU CP-B 62b), the method comprising steps for generating a first status word STWA in the first system CP-A and a parallel second status word STWB in the second system CP-B (Figure 4 of Dutton

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teaches first Integer Units 64a and FPU 66a for generating a first status word STWA in the first system CPU CP-A 62a and second Integer Units 64b and FPU 66b for generating a second status word STWB in the second system CPU CP-B 62b), generating in the first system CP-A a first check code CCA from the first status word STWA (Signature Generator 68a in Figure 4 of Dutton generates a first signature check code CCA from the first status word STWA in the first system CPU CP-A 62a); and generating in the second system CP-B a second check code CCB from the second status word STWB (Signature Generator 68b in Figure 4 of Dutton generates a second signature check code CCB from the second status word STWB in the second system CPU CP-B 62b), wherein said method is characterized by the further steps of transferring the first check code CCA from the first system CP-A to the second system CP-B (col. 7, lines 63-64 in Dutton teach that Comparator 36 can be included in one or both of CPU CP-A 62a and CPU CP-B 62b, hence Dutton teaches that the first signature check code CCA is transferred from the first system CPU CP-A 62a to the second system CPU CP-B 62b in the case that Comparator 36 is included in CPU CP-B 62b).

The prior art however are not concerned with and do not teach, suggest, or otherwise render obvious a step for recreating the first data word STWA in the second system CP-B by evaluating the first check code CCA, the second check code CCB and the second data word STWB as taught by claim 1. Hence the prior art taken alone or in any combination fail to teach the claimed novel feature in claim 1.

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Claim 7 recites substantially similar language as in claim 1, hence is allowed for the same reason.

Claims 2-6 and 8-11 depend from respective claims 1 and 7.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

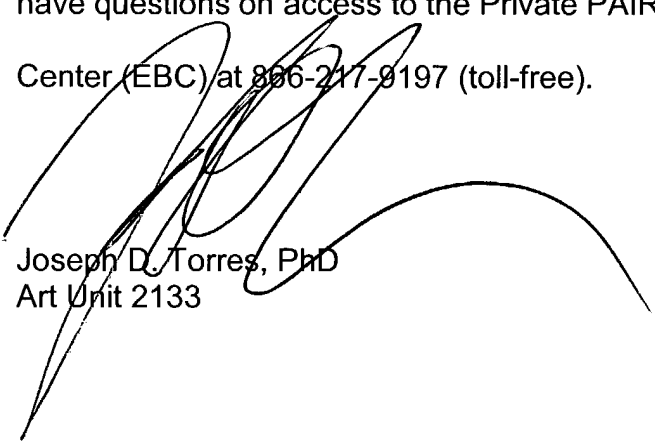
The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Krings; Lothar (US 4819232 A) teaches a fault-tolerant multiprocessor with a memory for storing the previous data state. Ogisu; Yukihsa et al. (US 5446875 A) teaches a computer system whose back-up process takes tasks over continually from an actual process at failure by passing through information stored in a shared memory (such as a non-volatile shared memory) from the actual process to the back-up process. Williams; Emrys John (US 5799022 A) teaches lockstep fault tolerant computers which use multiple subsystems that run identically.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Joseph D. Torres whose telephone number is (703) 308-7066. The examiner can normally be reached on M-F 8-5.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Albert Decady can be reached on (703) 305-9595. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Joseph D. Torres, PhD
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